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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Y. SHIMADA, et al.

Serial No.: 10/521,470

Filed: January 18, 2005

Title: MULTILAYER WIRING BOARD, MANUFACTURING METHOD THEREOF, SEMICONDUCTOR DEVICE, AND WIRELESS ELECTRONIC DEVICE

Examiner: Unassigned

Group Art Unit: Unassigned

Confirmation No.: Unassigned

SUBMISSION OF ENGLISH TRANSLATION OF  
INTERNATIONAL PRELIMINARY EXAMINATION REPORT

**Mail Stop: Amendment**

Commissioner for Patents

P.O. Box 1450

Alexandria, Virginia 22313-1450

May 25, 2005

SIR:

Enclosed please find a copy of the English translation of the International Preliminary Examination Report for International (PCT) Application No. PCT/JP2003/006860, filed May 30, 2003, of which the above-identified application is a National Stage Application under 35 USC 371.

Please note that each of the documents (Documents 1-6) referred to in the enclosed English translation of the International Preliminary Examination Report has previously been provided to the Examiner and listed in the Information Disclosure Statement submitted January 18, 2005, in the above-identified application, upon initially filing the above-identified application. Please note that the enclosed English translation provides an additional concise description of documents provided on a previously submitted list, for satisfying 37 CFR 1.98(a)(3) in connection with these documents.

Consideration of the enclosed English translation of the International Preliminary Examination Report for International (PCT) Application No. PCT/JP2003/006860, upon examination of the above-identified application, is respectfully requested.

Applicants request any shortage or excess in fees in connection with the filing of this paper be charged or credited to Deposit Account No. 01-2135 (case 1204.44657X00).

Respectfully submitted,

**ANTONELLI, TERRY, STOUT & KRAUS, LLP**

By

  
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William I. Solomon  
Registration No. 28,565

WIS/ksh  
1300 N. Seventeenth Street  
Suite 1800  
Arlington, Virginia 22209  
Tel: 703-312-6600  
Fax: 703-312-6666